



Session : **MIMO Hardware and Rapid Prototyping (SPECIAL SESSION)**

Time: 10:50 – 12:50

Chair:	<i>Thomas Kaiser , Universitaet Essen</i>
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A FLEXIBLE MIMO TESTBED WITH REMOTE ACCESS ([Abstract](#))

Christian Mehlführer (Vienna University of Technology, Austria)
Stefan Geirhofer (Vienna University of Technology, Austria)
Sebastian Caban (Vienna University of Technology, Austria)
Markus Rupp (Vienna University of Technology, Austria)



OPTIMIZED FFT ARCHITECTURE FOR MIMO APPLICATIONS ([Abstract](#))

Ludwig Schwoerer (Nokia Research Center, Germany)
Ernst Zielinski (Nokia Research Center, Germany)

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DESIGN OF THE TRANSIT ACCESS POINT HARDWARE PLATFORM ([Abstract](#))

Patrick Murphy (Rice University, United States)

J. Patrick Frantz (Rice University, United States)

Behnaam Aazhang (Rice University, United States)

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AN EXEMPLARY COMPARISON OF PER ANTENNA RATE CONTROL BASED MIMO-HSDPA RECEIVERS (Abstract)

Qipeng Cai	(University Duisburg–Essen, Germany)
Andreas Wilzeck	(University Duisburg–Essen, Germany)
Christoph Schindler	(University Duisburg–Essen, Germany)
Steffen Paul	(Infineon Technologies AG, Germany)
Thomas Kaiser	(University Duisburg–Essen, Germany)

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 **MULTI-STANDARD DEVELOPMENT AND MEASURING PLATFORM FOR MIMO-SOFTWARE DEFINED RADIO ([Abstract](#))**

Georg Meindl-Pfeiffer (ARC Seibersdorf research GmbH, Austria)
Reinhard Kloibhofer (ARC Seibersdorf research GmbH, Austria)
Florian Kaltenberger (ARC Seibersdorf research GmbH, Austria)
Gerhard Humer (ARC Seibersdorf research GmbH, Austria)

 **LOW COMPLEXITY ITERATIVE RECEIVERS FOR SPACE-TIME BLOCK CODED MC-CDMA DOWNLINK SYSTEMS ([Abstract](#))**

Alberto Jiménez Pacheco (Universidad Politécnica de Madrid, Spain)
Mariano García Otero (Universidad Politécnica de Madrid, Spain)

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