



Session : **Hardware Implementations of DSP Algorithms**

Time: 10:00 – 11:20

Chair: *Levent Oktem , Synplicity Inc*



SEGMENT-BASED MOTION ESTIMATION USING A BLOCK-BASED ENGINE ([Abstract](#))

Patrick Meuwissen (Philips Research, Netherlands)
Fabian Ernst (Philips Research, Netherlands)
Ramanathan (Philips Research, Netherlands)
Sethuraman (Philips Research, Netherlands)
Harm Peters (Philips Research, Netherlands)
Rafael Peset Llopis (Philips Consumer Electronics, Netherlands)

[Back](#)

[Menu](#)

[Next](#)



Session : **Hardware Implementations of DSP Algorithms**

Time: 10:00 – 11:20

Chair:	<i>Levent Oktem , Synplicity Inc</i>
---------------	--------------------------------------



A LECTURE COURSE SERIES: FROM CONCEPT ENGINEERING TO IMPLEMENTATION OF SIGNAL PROCESSING ALGORITHMS WITH FPGAS ([Abstract](#))

Mario Huemer (University of Erlangen–Nuremberg, Germany)
Michael Lunglmayr (University of Applied Sciences of Upper Austria Hagenberg, Austria)
Markus Pfaff (University of Applied Sciences of Upper Austria Hagenberg, Austria)



EFFICIENT FPGA IMPLEMENTATION OF AN ADAPTIVE IQ-IMBALANCE CORRECTOR FOR COMMUNICATION RECEIVERS USING REDUCED RANGE MULTIPLIERS ([Abstract](#))

Ediz Cetin (University of Westminster, United Kingdom)
Suleyman Demirsoy (University of Westminster, United Kingdom)
Izzet Kale (University of Westminster, United Kingdom)
Richard Morling (University of Westminster, United Kingdom)

[Back](#)

[Menu](#)

[Next](#)



Session : Hardware Implementations of DSP Algorithms

Time: 10:00 – 11:20

Chair:	<i>Levent Oktem , Synplicity Inc</i>
---------------	--------------------------------------



DESIGN AND IMPLEMENTATION OF A NEW PERSIAN DIGITS OCR ALGORITHM ON FPGA CHIPS

(Abstract)

Navid Toosizadeh
Mohammad Eshghi

(Shahid Beheshti University, Iran)
(Shahid Beheshti University, Iran)

[Back](#)

[Menu](#)